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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION OF

TSUJI, et al.

Group Art Unit: Not Assigned

Appln. No.: Not Assigned

Examiner: Not Assigned

Filed: October 18, 2001

Title: SEMICONDUCTOR INPUT PROTECTION CIRCUIT



Date: October 18, 2001

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INFORMATION DISCLOSURE STATEMENT

The attached Information Disclosure Statement is being filed concurrently with the application for patent.

This IDS is intended to be in full compliance with the rules, but should the Examiner find any part of its required content to have been omitted, prompt notice to that effect is earnestly solicited, along with additional time under Rule 97(f), to enable Applicant to comply fully.

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Respectfully submitted,

Pillsbury Winthrop LLP

A handwritten signature in dark ink, appearing to read "Roger R. Wise".

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